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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/069,523	06/12/2002	Shogo Ishioka	020238	9379	
38834	7590 02/16/2005		EXAMINER		
WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP			DOLE, TII	DOLE, TIMOTHY J	
1250 CONNECTICUT AVENUE, NW SUITE 700		ART UNIT	PAPER NUMBER		
WASHINGTO	N, DC 20036		2858	·	

DATE MAILED: 02/16/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		(SM)
	Application No.	Applicant(s)
	10/069,523	ISHIOKA ET AL.
Office Action Summary	Examiner	Art Unit
	Timothy J. Dole	2858
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	6(a). In no event, however, may a reply be tin within the statutory minimum of thirty (30) day ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed on 10 De	ecember 2004.	•
<i>,</i> —	action is non-final.	;
3) Since this application is in condition for allowan		
closed in accordance with the practice under E	х рапе Quayle, 1935 С.D. 11, 4:	53 O.G. 213.
Disposition of Claims		
4) Claim(s) 1-16 is/are pending in the application. 4a) Of the above claim(s) 10-16 is/are withdraw 5) Claim(s) is/are allowed. 6) Claim(s) 1-9 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or	n from consideration.	· · · · · · · · · · · · · · · · · · ·
Application Papers		
9) The specification is objected to by the Examine 10) The drawing(s) filed on 05 March 2002 is/are: a Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Ex	a)⊠ accepted or b)□ objected to drawing(s) be held in abeyance. Sec on is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
a) ☐ All b) ☐ Some * c) ☒ None of: 1. ☒ Certified copies of the priority documents 2. ☐ Certified copies of the priority documents 3. ☐ Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list of the certified copies of the attached detailed Office action for a list of the certified copies of the priorical bureau.	s have been received. s have been received in Applicati ity documents have been receive (PCT Rule 17.2(a)).	on No ed in this National Stage
Attack-mant/a)		
Attachment(s) 1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)
2) Notice of Preferences Oried (1 10-032) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No(s)/Mail Da	

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-3 and 5-9 are rejected under 35 U.S.C. 102(b) as being anticipated by Soiferman.

Referring to claims 1 and 9, Soiferman discloses an inspection apparatus for inspecting a circuit wiring of a circuit board (abstract), said inspection apparatus comprising: a conductive member (fig. 2 (10)) adapted to be disposed on the side of one of the surfaces of said circuit board (fig. 2 (16)) and to be supplied with an inspection signal (column 4, lines 22-29); means for supplying the inspection signal to said conductive member (fig. 1 (11)); a plurality of cells (fig. 2 (12) and column 4, lines 52-54) adapted to be disposed on the side of the other surface of said circuit board (fig. 2); and means for acquiring each signal appearing at said cells in response to said inspection signal applied to said conductive member (column 5, lines 16-19).

Referring to claim 2, Soiferman discloses the apparatus as claimed wherein said conductive member includes a surface formed in conformity with said one surface of said circuit board (fig. 2), and said cells are two-dimensionally arranged in conformity with said other surface of said circuit board (fig. 2 and column 4, lines 41-43).

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Referring to claim 3, Soiferman discloses the apparatus as claimed wherein said conductive member has a flat plate shape (fig. 2).

Referring to claim 5, Soiferman discloses the apparatus as claimed wherein said cells are arranged in a matrix form (column 7, lines 1-3).

Referring to claim 6, Soiferman discloses the apparatus as claimed which further includes: means for generating image data representing the position and shape of said circuit wiring, according to the signals appearing at said cells (column 5, lines 35-47); and means for displaying the image (column 5, lines 47-49).

Referring to claim 7, Soiferman discloses the apparatus as claimed which further includes storing means having thereon stored wiring data representing the position and shape of said circuit wiring (column 5, lines 60-61).

Referring to claim 8, Soiferman discloses the apparatus as claimed which further includes: storing means having thereon stored wiring data representing the position and shape of the circuit wiring (column 5, lines 60-61); means for detecting a disconnection, short-circuit or chipping in said circuit wiring, or a dust on said circuit board according to the signals appearing at said cells and said wiring data (column 5, lines 50-58).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

4. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Soiferman in view of Cilingiroglu.

Referring to claim 4, Soiferman discloses the apparatus as claimed except wherein said conductive member is composed of a plurality of conductive pieces.

Cilingiroglu discloses an inspection apparatus wherein said conductive member is composed of a plurality of conductive pieces (fig. 5 (502-504)).

Therefore, it would have been obvious to one skilled in the art at the time of the invention to incorporate the plurality of conductive pieces of Cilingiroglu into the apparatus of Soiferman for the purpose of making the apparatus more versatile by making it possible to only provide testing at desired locations.

Response to Arguments

5. Applicant's arguments, see pages 8-10, filed December 10, 2004, with respect to the rejection(s) of claim(s) 1-9 under Ishioka et al. have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Soiferman and Cilingiroglu.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Timothy J. Dole whose telephone number is (571) 272-2229. The examiner can normally be reached on Mon. thru Fri. from 8:00 to 4:30.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward Lefkowitz can be reached on (571) 272-2180. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TJD

TA, M

ANJAN DEB PRIMARY EXAMINER

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